

Docket No.: 57454-138

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Yasuhiko TSUKIKAWA

Serial No.: 09/877,027 ✓

Filed: June 11, 2001 ✓

For: CONFIGURATION FOR GENERATING A CLOCK INCLUDING A DELAY
CIRCUIT AND METHOD THEREOF

Group Art Unit: 2811

Examiner: Linh M. NGUYEN

AMENDMENT UNDER 37 C.F.R. 1.111

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

The following amendment and remarks are submitted in response to the Official Action mailed January 25, 2002. A mark-up version showing amendment made is also attached hereto.

IN THE SPECIFICATION

Please replace the paragraph starting from page 24, line 2, with the following paragraph:

--Reference will now be made to Fig. 16 to describe an exemplary configuration of a semiconductor device 20000 having DLL 2000 of the second embodiment. Semiconductor device 20000, as shown in Fig. 16, includes DLL 2000 receiving external clocks CLKext and ZCLKext, an input buffer 2001 receiving a control signal (a row address strobe signal /RAS, a column address strobe signal/CAS, a chip select signal /CS, a write enable signal /WE and the like), an input buffer 2002 receiving an address signal